

REVISION HISTORY

Revision V1.0

Initial release



Swiss. Embedded. Computing.

Title OSM SoM Carrier Board Adapter			Toradex AG Ebenastrasse 10 Horw 6048 Switzerland
Size: A3	Number:1	Revision:V1.0	
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1	2	3	4	5	6	7	8
ADC 0	ADC0_0	X1A	JTAG_TDO(SWO)	JTAG_TDO(SWO)	X1GH		
ADC 1	ADC0_1	X1B	JTAG_TMS(SWDIO)	JTAG_TMS(SWDIO)	X1GI		
BOOT_SEL0#	BOOT_SEL0#	X1U	PWM 1	PWM 1	X1GV		
BOOT_SEL1#	BOOT_SEL1#	X1V	PWM 2	PWM 2	X1GW		
CAN A RX	CAN_A_RX	X1AB	PWM 3	PWM 3	X1GX		
CAN A TX	CAN_A_TX	X1AC	PWM 4	PWM 4	X1GY		
CAN B RX	CAN_B_RX	X1AD	PWM 5	PWM 5	X1GZ		
CAN B TX	CAN_B_TX	X1AE	RESERVED1	RESERVED1	X1HB		
CARRIER_PWR_EN	CARRIER_PWR_EN	X1AF	RESET_IN#	RESET_IN#	X1HE		
CARRIER_STBY#	CARRIER_STBY#	X1AG	RESET_OUT#	RESET_OUT#	X1HF		
CH0_ACT	CH0_ACT	X1G	RTC_PWR	RTC_PWR	X1IF		
CH0_LINK	CH0_LINK	X1C	SDIO_A_CD#	SDIO_A_CD#	X1IG		
CH0_RX_N	CH0_RX_N	X1H	SDIO_A_CLK	SDIO_A_CLK	X1IH		
CH0_RX_P	CH0_RX_P	X1I	SDIO_A_CMD	SDIO_A_CMD	X1II		
CH0_RXC	CH0_RXC	X1E	SDIO_A_D0	SDIO_A_D0	X1IJ		
CH0_SYNC_TRIGGER	CH0_SYNC_TRIGGER	X1E	SDIO_A_D1	SDIO_A_D1	X1IK		
CH0_TX_N	CH0_TX_N	X1K	SDIO_A_D2	SDIO_A_D2	X1IL		
CH0_TX_P	CH0_TX_P	X1L	SDIO_A_D3	SDIO_A_D3	X1IM		
CH0_TXC	CH0_TXC	X1D	SDIO_A_IOPWR	SDIO_A_IOPWR	X1IN		
CH1_ACT	CH1_ACT	X1M	SDIO_A_PWR_EN	SDIO_A_PWR_EN	X1IO		
CH1_LINK	CH1_LINK	X1S	SDIO_A_WP	SDIO_A_WP	X1IP		
CH1_RX_N	CH1_RX_N	X1N	SDIO_B_CD#	SDIO_B_CD#	X1IQ		
CH1_RX_P	CH1_RX_P	X1O	SDIO_B_CLK	SDIO_B_CLK	X1IR		
CH1_RXC	CH1_RXC	X1T	SDIO_B_CMD	SDIO_B_CMD	X1IS		
CH1_SYNC_TRIGGER	CH1_SYNC_TRIGGER	X1P	SDIO_B_D0	SDIO_B_D0	X1IT		
CH1_TX_N	CH1_TX_N	X1Q	SDIO_B_D1	SDIO_B_D1	X1IU		
CH1_TX_P	CH1_TX_P	X1R	SDIO_B_D2	SDIO_B_D2	X1IV		
CH1_TXC	CH1_TXC	X1F	SDIO_B_D3	SDIO_B_D3	X1IW		
DEBUG_EN	DEBUG_EN	X1AR	SDIO_B_D4	SDIO_B_D4	X1IX		
ETH_A (R)(G)MII_COL	ETH_A (R)(G)MII_COL	X1BM	SDIO_B_D5	SDIO_B_D5	X1IY		
ETH_A (R)(G)MII_CRS	ETH_A (R)(G)MII_CRS	X1BN	SDIO_B_D6	SDIO_B_D6	X1IZ		
ETH_A (R)(G)MII_RX_CLK	ETH_A (R)(G)MII_RX_CLK	X1BQ	SDIO_B_D7	SDIO_B_D7	X1JA		
ETH_A (R)(G)MII_RX_DV(ER)	ETH_A (R)(G)MII_RX_DV(ER)	X1BR	SDIO_B_IOPWR	SDIO_B_IOPWR	X1JB		
ETH_A (R)(G)MII_RX_ER	ETH_A (R)(G)MII_RX_ER	X1BS	SDIO_B_PWR_EN	SDIO_B_PWR_EN	X1JC		
ETH_A (R)(G)MII_RXD2	ETH_A (R)(G)MII_RXD2	X1BO	SDIO_B_WP	SDIO_B_WP	X1JD		
ETH_A (R)(G)MII_RXD3	ETH_A (R)(G)MII_RXD3	X1BP	SPI_A_CS0#	SPI_A_CS0#	X1JE		
ETH_A (R)(G)MII_TX_CLK	ETH_A (R)(G)MII_TX_CLK	X1BT	SPI_A_CS1#/GPIO_A_6	SPI_A_CS1#/GPIO_A_6	X1JF		
ETH_A (R)(G)MII_TX_EN(ER)	ETH_A (R)(G)MII_TX_EN(ER)	X1BU	SPI_A_HOLD (IO3)	SPI_A_HOLD (IO3)	X1JG		
ETH_A (S)(R)(G)MII_RX_P	ETH_A (S)(R)(G)MII_RX_P	X1BG	SPI_A_SCK	SPI_A_SCK	X1JH		
ETH_A (S)(R)(G)MII_RX_N	ETH_A (S)(R)(G)MII_RX_N	X1BH	SPI_A_SDI (IO1)	SPI_A_SDI (IO1)	X1JI		
ETH_A (S)(R)(G)MII_TX_P	ETH_A (S)(R)(G)MII_TX_P	X1BI	SPI_A_SDO (IO0)	SPI_A_SDO (IO0)	X1JJ		
ETH_A (S)(R)(G)MII_TX_N	ETH_A (S)(R)(G)MII_TX_N	X1BJ	SPI_A_WP (IO2)	SPI_A_WP (IO2)	X1JK		
ETH_A (S)(R)(G)MII_CLK_P	ETH_A (S)(R)(G)MII_CLK_P	X1BK	SPI_B_CS0#	SPI_B_CS0#	X1JL		
ETH_A (S)(R)(G)MII_CLK_N	ETH_A (S)(R)(G)MII_CLK_N	X1BL	SPI_B_CS1#/GPIO_A_7	SPI_B_CS1#/GPIO_A_7	X1JM		
ETH_A_SDP	ETH_A_SDP	X1BV	SPI_B_SCK	SPI_B_SCK	X1JN		
ETH_IOPWR	ETH_IOPWR	X1CO	SPI_B_SDI	SPI_B_SDI	X1JO		
ETH_MDC	ETH_MDC	X1CP	SPI_B_SDO	SPI_B_SDO	X1JP		
ETH_MDIO	ETH_MDIO	X1CQ	TEST_GENERIC	TEST_GENERIC	X1JQ		
FORCE_RECOVERY#	FORCE_RECOVERY#	X1CR	UART_A_CTS	UART_A_CTS	X1JR		
GND	GND	X1DN	UART_A_RTS	UART_A_RTS	X1JS		
GND	GND	X1DO	UART_A_RX	UART_A_RX	X1JS		
GND	GND	X1DP	UART_A_TX	UART_A_TX	X1JT		
GND	GND	X1DQ	UART_B_CTS	UART_B_CTS	X1JU		
GND	GND	X1DR	UART_B_RTS	UART_B_RTS	X1JU		
GND	GND	X1DS	UART_B_RX	UART_B_RX	X1JV		
GND	GND	X1DU	UART_B_TX	UART_B_TX	X1JW		
GND	GND	X1DV	UART_C_RX	UART_C_RX	X1JX		
GND	GND	X1DT	UART_C_TX	UART_C_TX	X1JY		
GND	GND	X1DX	UART_CON_RX	UART_CON_RX	X1KB		
GND	GND	X1DW	UART_CON_TX	UART_CON_TX	X1KC		
GND	GND	X1DY	UART_D_RX	UART_D_RX	X1JZ		
GND	GND	X1DZ	UART_D_TX	UART_D_TX	X1KA		
GND	GND	X1EA	USB_A_D_N	USB_A_D_N	X1KD		
GND	GND	X1EB	USB_A_D_P	USB_A_D_P	X1KE		
GND	GND	X1EC	USB_A_EN	USB_A_EN	X1KF		
GND	GND	X1ED	USB_A_ID	USB_A_ID	X1KG		
GND	GND	X1EE	USB_A_OC#	USB_A_OC#	X1KH		
GND	GND	X1EF	USB_A_VBUS	USB_A_VBUS	X1KI		
GND	GND	X1EG	USB_B_D_N	USB_B_D_N	X1KJ		
GND	GND	X1EH	USB_B_D_P	USB_B_D_P	X1KK		
GND	GND	X1EI	USB_B_EN	USB_B_EN	X1KL		
GPIO_A_0	GPIO_A_0	X1EY	USB_B_ID	USB_B_ID	X1KM		
GPIO_A_1	GPIO_A_1	X1EZ	USB_B_OC#	USB_B_OC#	X1KN		
GPIO_A_2	GPIO_A_2	X1FA	USB_B_VBUS	USB_B_VBUS	X1KO		
GPIO_A_3	GPIO_A_3	X1FB	V_BAT	V_BAT	X1KP		
GPIO_A_4	GPIO_A_4	X1FC	V_BAT	V_BAT	X1KQ		
GPIO_A_5	GPIO_A_5	X1FD	VCC_2_TEST2	VCC_2_TEST2	X1LS		
GPIO_B_0	GPIO_B_0	X1FE	VCC_3_TEST	VCC_3_TEST	X1LT		
GPIO_B_1	GPIO_B_1	X1FF	VCC_4_TEST	VCC_4_TEST	X1LB		
GPIO_B_2	GPIO_B_2	X1FG	VCC_IN_3V3	VCC_IN_3V3	X1LC		
GPIO_B_3	GPIO_B_3	X1FH	VCC_IN_5V	VCC_IN_5V	X1LD		
GPIO_B_4	GPIO_B_4	X1FI	VCC_OUT_IO	VCC_OUT_IO	X1LE		
GPIO_B_5	GPIO_B_5	X1FJ	Vendor Defined 1	Vendor Defined 1	X1LJ		
GPIO_B_6	GPIO_B_6	X1FK	Vendor Defined 2	Vendor Defined 2	X1LM		
GPIO_B_7	GPIO_B_7	X1FL	Vendor Defined 3	Vendor Defined 3	X1LN		
I2C_A_SCL	I2C_A_SCL	X1FQ	CAM_A_PWR/GPIO_C_6	CAM_A_PWR/GPIO_C_6	X1LO		
I2C_A_SDA	I2C_A_SDA	X1FR	CAM_A_RST#/GPIO_C_7	CAM_A_RST#/GPIO_C_7	X1LR		
I2C_B_SCL	I2C_B_SCL	X1FS	CAM_A_SCL/CSI_A_TX_P	CAM_A_SCL/CSI_A_TX_P	X1W		
I2C_B_SDA	I2C_B_SDA	X1FT	CAM_A_SDA/CSI_A_TX_N	CAM_A_SDA/CSI_A_TX_N	X1X		
I2S_A_BITCLK	I2S_A_BITCLK	X1FU	CAM_MCK	CAM_MCK	X1Y		
I2S_A_DATA_IN	I2S_A_DATA_IN	X1FV	CSI_A_CLOCK_N	CSI_A_CLOCK_N	X1Z		
I2S_A_DATA_OUT	I2S_A_DATA_OUT	X1FW	CSI_A_CLOCK_P	CSI_A_CLOCK_P	X1AA		
I2S_A_LRCLK	I2S_A_LRCLK	X1FX	CSI_A_DATA0_N	CSI_A_DATA0_N	X1AH		
I2S_B_BITCLK	I2S_B_BITCLK	X1FY	CSI_A_DATA0_P	CSI_A_DATA0_P	X1AI		
I2S_B_DATA_IN	I2S_B_DATA_IN	X1FZ	CSI_A_DATA1_N	CSI_A_DATA1_N	X1AJ		
I2S_B_DATA_OUT	I2S_B_DATA_OUT	X1GA	CSI_A_DATA1_P	CSI_A_DATA1_P	X1AK		
I2S_B_LRCLK	I2S_B_LRCLK	X1GB	CSI_A_DATA2_P	CSI_A_DATA2_P	X1AL		
I2S_MCLK	I2S_MCLK	X1GC	CSI_A_DATA3_N	CSI_A_DATA3_N	X1AM		
JTAG_nTRST	JTAG_nTRST	X1GD	CSI_A_DATA3_P	CSI_A_DATA3_P	X1AN		
JTAG_RTCK	JTAG_RTCK	X1GE	DISP_BL_EN/GPIO_C_5	DISP_BL_EN/GPIO_C_5	X1AO		
JTAG_TCK(SWCLK)	JTAG_TCK(SWCLK)	X1GF			X1AP		
JTAG_TDI	JTAG_TDI	X1GG			X1AQ		
					X1AS		

[illegible]

1	2	3	4	5	6	7	8			
A	M1A		M1B		M1C		M1D		M1H	
	GND	A1	GND	B1	GND	C1	GND	D1	SDIO B PWR EN	D51
	LVDS_CLK_P	A2	RGB_R4/LVDS_CLK_P	B2	RGB_G0/LVDS_D3_N	RGB_G1	C2	RGB_G1	I2S_B_BITCLK	D52
	LVDS_CLK_N	A3	RGB_R2/LVDS_CLK_N	B3	RGB_G2/LVDS_D3_P	RGB_G3	C3	RGB_G3	SDIO B_IOPWR	D53
	GND	A4	GND	B4	GND	GND	C4	GND	GND	D54
	RGB_G4	A5	RGB_G4	B5	RGB_G5/LVDS_D2_N		C5	ETH_B (R)(G)MII_RXD2	SDIO_B_D5	D55
	RGB_B1	A6	RGB_B1	B6	RGB_B0/LVDS_D2_P		C6	GND	SDIO_B_D3	D56
	GND	A7	GND	B7	GND		C7	ETH_B_SDP	GND	D57
	RGB_B2	A8	RGB_B2	B8	RGB_B4/LVDS_D1_N		C8	ETH_B_(R)(G)MII_RX_DV(ER)	SDIO_B_CD#	D58
	RGB_B3	A9	RGB_B3	B9	RGB_(PIXEL)CLK/LVDS_D1_P		C9	GND	SDIO_B_D7	D59
	GND	A10	GND	B10	GND	GND	C10	ETH_B_(S)(R)(G)MII_RXD1	GND	D60
	RGB_B5	A11	RGB_B5	B11	RGB_HSYNC	RGB_HSYNC	C11	ETH_B_(S)(R)(G)MII_RX_P	SDIO_B_D6	D61
	RGB_VSYNC	A12	RGB_VSYNC	B12	GND	GND	C12	GND	SDIO_B_D4	D62
	RGB_RESET#	A13	RGB_RESET#	B13	RGB_DISP/LVDS_D0_N	GND	C13	ETH_B_(S)(R)(G)MII_TXD0	GND	D63
	GND	A14	GND	B14	RGB_DE/LVDS_D0_P	ETH_B_(R)(G)MII_TX_EN(ER)	C14	ETH_B_(S)(R)(G)MII_TXD1	SDIO_B_D0	D64
	RGB_CS#	A15	RGB_CS#	B15	GND	ETH_B_(R)(G)MII_COL	C15	GND	SDIO_B_CLK	D65
	ETH_B_(R)(G)MII_CRS	A16	ETH_B_(R)(G)MII_CRS	B16	CAM_A_PWR/ GPIO_C_6	GND	C16	ETH_B_(S)(R)(G)MII_TXD2	GND	D66
	GND	A17	GND	B17	CAM_MCK	GND	C17	ETH_B_(S)(R)(G)MII_TXD3	SDIO_B_D2	D67
	CSI_A_DATA0_N	A18	CSI_A_DATA0_N	B18	GND	GND	C18	GND	SDIO_B_D1	D68
	CSI_A_DATA0_P	A19	CSI_A_DATA0_P	B19	CSI_A_DATA1_N	CAM_A_RST#/ GPIO_C_7	C19	DISP_VDD_EN/ GPIO_C_4	GND	D69
B	GND	A20	GND	B20	CSI_A_DATA1_P	GPIO_C_0	C20	CAM_A_SDA/ CSI_A_TX_N	SDIO_B_CMD	D70
	CSI_A_CLOCK_N	A21	CSI_A_CLOCK_N	B21	GND	GPIO_C_3	C21	GPIO_C_2	SDIO_A_CD#	D71
	CSI_A_CLOCK_P	A22	CSI_A_CLOCK_P	B22	CSI_A_DATA2_N	GND	C22	CAM_A_SCL/ CSI_A_TX_P	GND	D72
	GND	A23	GND	B23	CSI_A_DATA2_P		C23	GND		D73
	CSI_A_DATA3_N	A24	CSI_A_DATA3_N	B24	GND		C24	GPIO_C_1		D74
	CSI_A_DATA3_P	A25	CSI_A_DATA3_P	B25	USB_C_SSTX_P	GND	C25	ETH_A_(R)(G)MII_TX_CLK		D75
	GND	A26	GND	B26	USB_C_SSTX_N	VCC_6_TEST	C26	DISP_BL_EN/ GPIO_C_5		D76
	ETH_A_(S)(R)(G)MII_RX_P	A27	ETH_A_(S)(R)(G)MII_RXD0	B27	GND		C27	ETH_B_MDC		D77
	ETH_A_(S)(R)(G)MII_RX_N	A28	ETH_A_(S)(R)(G)MII_RXD1	B28	USB_C_EN	Vendor Defined 4	C28	GND		D78
	GND	A29	GND	B29	GND	GND	C29	ETH_A_(R)(G)MII_TX_EN(ER)		D79
	USB_C_SSRX_P	A30	USB_C_SSRX_P	B30	ETH_A_(S)(R)(G)MII_CLK_N	Vendor Defined 5	C30	ETH_B_MDIO		D80
	USB_C_SSRX_N	A31	USB_C_SSRX_N	B31	ETH_A_(S)(R)(G)MII_CLK_P		C31	GND		D81
	GND	A32	GND	B32	GND		C32	ETH_A_(R)(G)MII_RXD2		D82
	USB_C_D_P	A33	USB_C_D_P	B33	ETH_A_(R)(G)MII_RXD3		C33	ETH_A_(R)(G)MII_RX_CLK		D83
	USB_C_D_N	A34	USB_C_D_N	B34	UART_B_TX		C34	GND		D84
	GND	A35	GND	B35	GND		C35	ETH_A_(R)(G)MII_RX_ER		D85
	UART_B_RX	A36	UART_B_RX	B36	USB_C_ID		C36	ETH_MDIO		D86
	UART_A_RTS	A37	UART_A_RTS	B37	Vendor Defined 3		C37	ETH_A_SDP		D87
	GND	A38	GND	B38	JTAG_TDO(SWO)		C38	ETH_A_(R)(G)MII_COL		D88
	UART_A_TX	A39	UART_A_TX	B39	GND		C39	GND		D89
	UART_A_CTS	A40	UART_A_CTS	B40	SPI_B_CS1#/ GPIO_A_7		C40	ETH_A_(S)(R)(G)MII_TX_N		D90
	GND	A41	GND	B41	FORCE_RECOVERY#		C41	ETH_A_(S)(R)(G)MII_TX_P		D91
	CH1_RXC	A42	CH1_RXC	B42	ETH_IOPWR		C42	GND		D92
	UART_A_RX	A43	UART_A_RX	B43	GND		C43	ADC_1		D93
	GND	A44	GND	B44	CH1_RX_P		C44	BOOT_SEL1#		D94
	I2S_B_LRCLK	A45	I2S_B_LRCLK	B45	CH1_RX_N		C45	ADC_0		D95
	I2S_B_DATA_OUT	A46	I2S_B_DATA_OUT	B46	GND		C46	GND		D96
	I2S_B_DATA_IN	A47	I2S_B_DATA_IN	B47			C47	GPIO_A_2		D97
	GND	A48	GND	B48			C48	BOOT_SELO#		D98
	UART_B_CTS	A49	UART_B_CTS	B49	CH1_LINK		C49	SDIO_B_WP		D99
D	GPIO_A_1	A50	GPIO_A_1	B50	GND	Vendor Defined 2	C50	GND		D100
	APM6-100-06.5-L-04-0-A-TR		APM6-100-06.5-L-04-0-A-TR		APM6-100-06.5-L-04-0-A-TR		APM6-100-06.5-L-04-0-A-TR		APM6-100-06.5-L-04-0-A-TR	
C	M1E		M1F		M1G					
	GPIO_A_0	A51	GPIO_A_0	B51	JTAG_nTRST	C51	JTAG_nTRST	C51		
	GND	A52	GND	B52	JTAG_RTCK	C52	JTAG_RTCK	C52		
	CH1_TXC	A53	CH1_TXC	B53	GND	C53	GND	C53		
	GPIO_A_5	A54	GPIO_A_5	B54	JTAG_TMS(SWDIO)	C54	JTAG_TMS(SWDIO)	C54		
	GND	A55	GND	B55		C55	NC	C55		
	CH1_SYNC_TRIGGER	A56	CH1_SYNC_TRIGGER	B56		C56	NC	C56		
	CH0_SYNC_TRIGGER	A57	CH0_SYNC_TRIGGER	B57		C57	VCC_2_TEST2	C57		
	GND	A58	GND	B58		C58	NC	C58		
	CH0_RX_N	A59	CH0_RX_N	B59		C59	TEST_GENERIC	C59		
	CH0_RX_P	A60	CH0_RX_P	B60	GND	C60	GND	C60		
	GND	A61	GND	B61	DISP_BL_PWM/ PWM_0	C61	GPIO_B_7	C61		
	CH0_LINK	A62	CH0_LINK	B62	PWM_3	C62	GPIO_B_6	C62		
	CH0_ACT	A63	CH0_ACT	B63	GND	C63	GND	C63		
	GND	A64	GND	B64		C64	NC	C64		
	CH0_TX_N	A65	CH0_TX_N	B65		C65	NC	C65		
	CH0_TX_P	A66	CH0_TX_P	B66		C66	NC	C66		
	GND	A67	GND	B67		C67	NC	C67		
	UART_C_RX	A68	UART_C_RX	B68	GPIO_B_4	C68	SDIO_A_D2	C68		
	UART_D_RX	A69	UART_D_RX	B69	GPIO_B_0	C69	GND	C69		
	GND	A70	GND	B70	GND	C70	NC	C70		
	UART_C_TX	A71	UART_C_TX	B71	Vendor Defined 1	C71	NC	C71		
	UART_D_TX	A72	UART_D_TX	B72	CH0_RXC	C72	NC	C72		
	GND	A73	GND	B73	GPIO_B_1	C73	NC	C73		
	UART_CON_RX	A74	UART_CON_RX	B74	GPIO_B_2	C74	NC	C74		
	UART_CON_TX	A75	UART_CON_TX	B75	GPIO_B_3	C75	NC	C75		
	GND	A76	GND	B76	GND	C76	SDIO_A_IOPWR	C76		
		A77	eDP_A_LANE0_P	B77	NC	C77	SDIO_A_WP	C77		
		A78	eDP_A_LANE0_N	B78	NC	C78	GND	C78		
	GND	A79	GND	B79	NC	C79	SDIO_A_CMD	C79		
		A80	eDP_A_LANE2_P	B80	CH0_TXC	C80	SDIO_A_D0	C80		
		A81	eDP_A_LANE2_N	B81	VCC_8_TEST	C81	SDIO_A_PWR_EN	C81		
	GND	A82	GND	B82	GND	C82	SDIO_A_D3	C82		
		A83	eDP_A_LANE3_P	B83		C83	GND	C83		
		A84	eDP_A_LANE3_N	B84	eDP_A_LANE1_P	C84	GND	C84		
		A85	eDP_A_LANE3_N	B85	eDP_A_LANE1_N	C85	USB_D_EN	C85		
		A86	eDP_B_LANE0_P	B86	GND	C86	PCIe_B_PERST#	C86		
		A87	eDP_B_LANE0_N	B87	eDP_B_LANE1_P	C87	NC	C87		
		A88	GND	B88	eDP_B_LANE1_N	C88	GND	C88		
		A89	eDP_B_LANE2_P	B89	GND	C89	SPI_C_SCK	C89		
D		A90	eDP_B_LANE2_N	B90	NC	C90	SPI_C_SDO	C90		
		A91	GND	B91	eDP_B_LANE3_P	C91	eDP_A_BL_EN	C91		
		A92	PCIe_B_HSO0_P	B92	eDP_B_LANE3_N	C92	VCC_OUT_IO	C92		
		A93	PCIe_B_HSO0_N	B93	GND	C93	VCC_IN_5V	C93		
		A94	GND	B94	eDP_A_BL_PWM	C94	VCC_IN_5V	C94		
		A95	PCIe_B_HSI0_P	B95	eDP_A_AUX_SEL	C95	VCC_IN_5V	C95		
		A96	PCIe_B_HSI0_N	B96	GND	C96	VCC_IN_5V	C96		
		A97	GND	B97	eDP_A_HPD	C97	VCC_IN_5V	C97		
		A98	eDP_B_AUX_P	B98	eDP_B_BL_EN	C98	VCC_IN_5V	C98		
		A99	eDP_B_AUX_N	B99	GND	C99	VCC_IN_5V	C99		
	GND	A100	GND	B100	VCC_IN_5V	C100	VCC_IN_5V	C100		
APM6-100-06.5-L-04-0-A-TR		APM6-100-06.5-L-04-0-A-TR		APM6-100-06.5-L-04-0-A-TR		APM6-100-06.5-L-04-0-A-TR		APM6-100-06.5-L-04-0-A-TR		
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